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Active Protection System Compliance Plan

Will Norton and Shannon Durbin
TARDEC Hit Avoidance Development and Integration Team
Warren, MI
19 AUG 2010

| maintaining the data needed, and including suggestions for reducir | completing and reviewing the colle ig this burden, to Washington Head could be aware that notwithstanding | ction of information. Send commen quarters Services, Directorate for In: | ts regarding this burden estim formation Operations and Rep | ate or any other aspect orts, 1215 Jefferson Da | vis Highway, Suite 1204, Arlington | |
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Report Documentation Page

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Agenda



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- Current Situation
- Compliance Plan Overview
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- Implementation
- SIL Validation
- Example Tasks
- Questions/Feedback

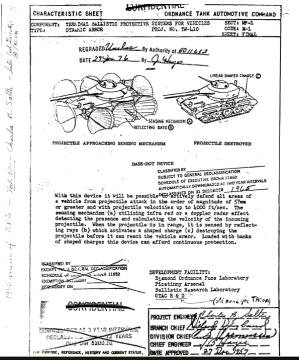


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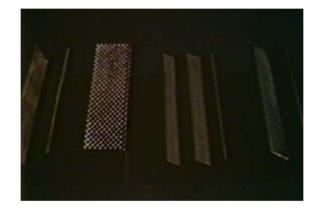
History of Active Protection

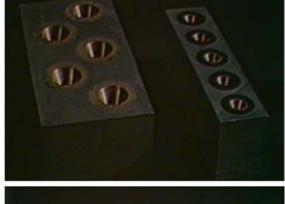


If you need a machine and don't buy it, then you will ultimately find that you have paid for it, but don't have it. - Henry Ford

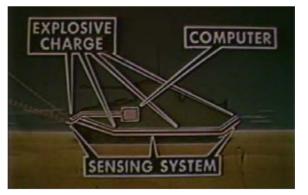


BLUF – Hard to develop a material solution without requirements and logical process to follow.









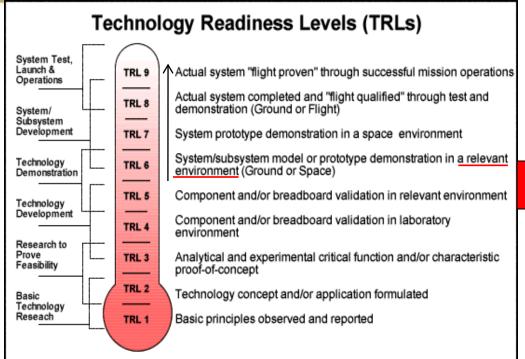




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Current Situation





DEPARTMENT OF DEFENSE **Technology Readiness** Assessment (TRA) Deskbook July 2009 Prepared by the Director, Research Directorate (DRD) Office of the Director, Defense Research and Engineering (DDR&E) This version of the TRA Deskbook accounts for policy and guidance provided by Directive DoDD 5000.01, of May 12, 2003 and certified current as of November 20, 2007;

PM's want:

TRL 6 or higher

Maturation of Existing Technologies

No major redesign activity (Major design activities complete)

Well understood systems (Performance, limitations, reliability, O&S)

"As an activity separate from the formal TRA, an early evaluation of technology maturity conducted shortly before Milestone A should be used to support the development of the Technology Development Strategy (TDS)."

"The law allows the MDA to waive the certification requirement (i.e., the technology in the program has been demonstrated in a relevant environment) if it determines that such a requirement would hinder the DoD's ability to meet critical national security objectives. As a matter of practice, such waivers will be granted only in extraordinary circumstances.7"

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Compliance Plan Overview



- (U) Compliance Plan Objectives
 - Estimate TRL of a specific system to a common standard
 - Verify TRL compliance to PMs interested in transition
 - Determine if the APS is mature enough to start a compliance effort
 - Establish specific tasks required to achieve each TRL milestone with respect to APS technologies as a function of gate review
- (U) The compliance plan uses specified requirements to measure and evaluate the maturity of:
 - Systems
 - Subsystems
 - Components
 - Software



Unclassified Attributes



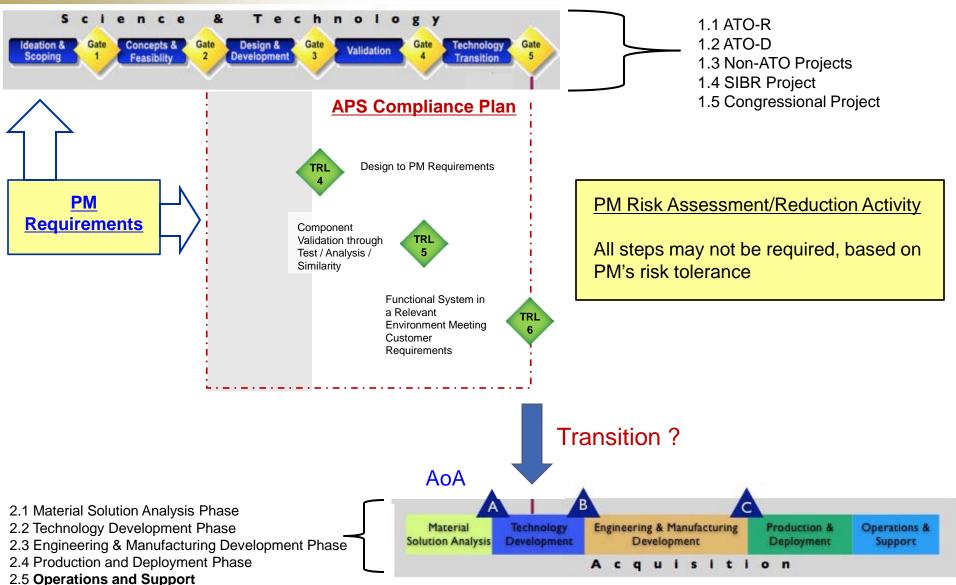
- (U) IS:
 - Detailed process to determine technical maturity
 - Comprehensive effort including government activities required to assure the APS works correctly/meets requirements for transition
 - Living document customized for each specific APS technology compliance program
 - Significant DoD investment each time it is exercised and the first time will be the most costly
- (U) IS NOT:
 - A developmental (R&D) effort
 - A program of record



Unclassified Program Structure



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Configuration Management

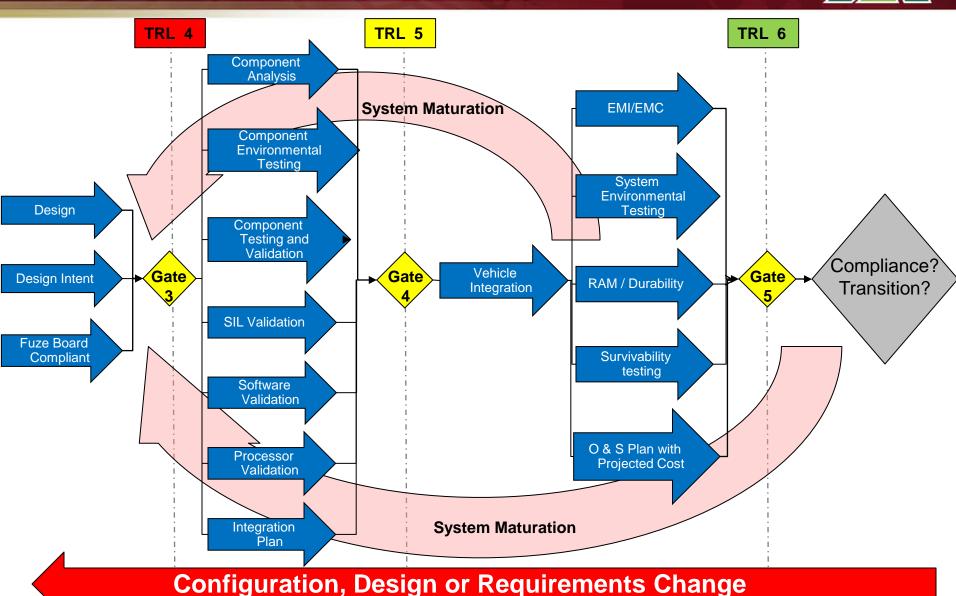


- (U) Compliance plans are assigned to specific APS configurations (hardware or software).
 - Possible to have different configurations at different TRLs.
- (U) If design changes are deemed necessary during the assessment a new compliance plan request is submitted.
 - Some elements may be accepted based on similarity but each element will be re-evaluated.
- (U) The compliance plan and requirements are living documents and anticipated to be configuration managed as well.



Unclassified Decision Process





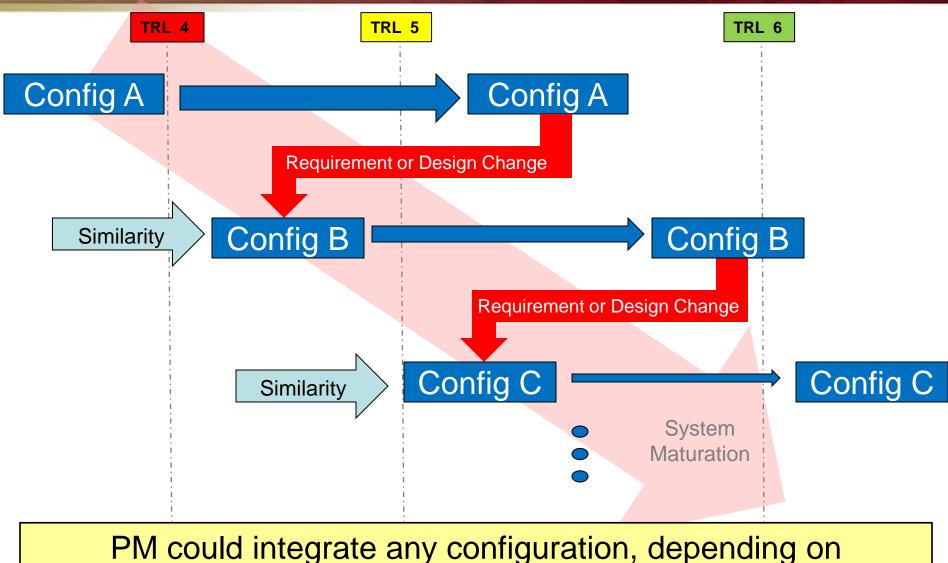
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Unclassified Implementation





acceptable performance risk



SIL Validation

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- (U) Software/Hardware-in-the-Loop Testing
- (U) Unit Testing
 - Test functions/classes within software to verify requirements are met
 - Physical space requirements, I/O pins/interface requirements, etc.
- (U) Integration Testing
 - Build and test specific vehicle integration concepts (Mapping sensors, Countermeasures, and no fire zones etc)
 - Verify software I/O message format is defined as per requirements; communicates with other systems outside of the 'black box' of the subsystem
 - Interfaces/communicates with other 'nodes' on the network
 - Countermeasure Simulators are considered where appropriate for Hardware in the loop validation.
 - Validate specific APS configurations
- (U) Validation Testing
 - Test functional requirements (software latency, etc.)
 - 'Bug' test- validate SW does not lock up/crash/perform incorrectly under various conditions
 - Meet functional requirements
 - Non-functional testing: load/robustness requirements, etc.
 - Validate interoperability with other connected systems
- (U) Not intended to be a physics based model



Unclassified

Example Tasks



| ask Name | TRL |
|--|-------|
| □ LRU Functional Block Diagram | TRL 4 |
| □ LRU Characteristics and Requirements | TRL 4 |
| Shake, Rattle, Roll, Speciality Analysis | TRL 5 |
| Architecture, Requirements, CM | TRL4 |
| ☐ Insensitive Munitions Tests | TRL 5 |
| Insensitive Munitions Board Approval Letter | TRL 6 |
| Fast cook-off | TRL 5 |
| Slow cook-off | TRL 5 |
| Bullet impact | TRL 5 |
| Fragment impact | TRL 5 |
| Sympathetic detonation | TRL 5 |
| Shaped charge | TRL 5 |
| High velocity fragment impact | TRL 5 |
| □ Counter Measure | TRL 5 |
| SMALL ARMS Protection (7.62 Ball) | TRL 5 |
| Environmental Tests | TRL 5 |
| Temperature Envelope | |
| Temperature Cycling | TRL 5 |
| Sand | TRL 5 |
| Humidity | TRL 5 |
| Salt Spray | TRL 5 |
| Vibration MIL-STD 810 | |
| Fly-Out Counter Measure | |
| Countermeasure Sensor (seeker) | |
| Range | |
| Timing | |
| Accuracy | |
| ☐ Airframe (SE) | |
| Safety factors and structural guidance as specified in MIL-M-8856B used for gui | TRL 5 |
| Netting analysis to verify motor case structural design. | TRL 6 |
| Finite element structural analyses to verify airframe integrity for flight testing. | |
| Modal analyses to determine mode shapes and frequencies. Results used to d | |
| Aero-elastic flutter analyses to determine flutter boundaries of airfoil surfaces us | |
| Static motor firings to characterize motor performance, including thrust and pres | |
| Hydro-burst tests of motor case to insure structural integrity. | |
| All airframe motor cases hydro-proofed prior to use in flight testing. | |
| Successful flight of airframe on LTV flights. | TRL6 |
| Successful flight of airframe on BTV flights. | TRL 6 |
| Successful flight of airframe on CTV flights. | TRL 6 |

| Name | TRL |
|---|-------|
| □ Comand and Control Processor | TRL 5 |
| Fuze Board Approval Letter | TRL 5 |
| Memory Requirements (ROM) | TRL 5 |
| Conforming Electrical Components | TRL 5 |
| Software Requirements | TRL 5 |
| Environmental | TRL 5 |
| Temperature Envelope | TRL 5 |
| Temperature Cycling | TRL 5 |
| Vibration MIL-STD-810 | TRL 6 |
| Temperature Limits (SE) | TRL 6 |
| Noise Factors (SE) | |
| Anticipated environmental limits/qualifications for each LRU (SE) | TRL 5 |
| Lightning strike requirements (SE) | TRL 5 |
| Convoy limitations (with the same APS systems installed) (SE) | TRL 5 |
| EMI/EMC Requirements | TRL 5 |
| SIL Processor Validation Testing | TRL 5 |
| Search Sensor | TRL 5 |
| EMI/EMC | TRL 5 |
| Clutter | TRL 5 |
| FOV | |
| Resolution | TRL 5 |
| Update Rate | TRL 5 |
| False Alarm Rate | TRL 5 |
| Optical | TRL 5 |
| Sensor Architecture | TRL 5 |
| Vibration (Microphonics, etc.) (SE) | TRL 5 |
| Tracking Sensor | TRL 5 |
| EMI/EMC | TRL 5 |
| Clutter | TRL 5 |
| FOV | TRL 6 |
| Resolution | TRL 6 |
| Update Rate | |
| False Alarm Rate | TRL 5 |
| Optical | TRL 4 |
| Sensor Architecture | TRL 4 |
| Environmental Tests | TRL 5 |
| Sensor Validation Testing | TRL 5 |







Questions?

Mailbox: DAMI_TARDEC_GSS_AP.C@conus.army.mil